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	Application No.	Applicant(s)
Notice of Allowability	09/635,217	TAMURA ET AL.
	Examiner	Art Unit
	Pierre-Michel Bataille	2186
The MAILING DATE of this communication appears on the cover sheet with the correspondence address All claims being allowable, PROSECUTION ON THE MERITS IS (OR REMAINS) CLOSED in this application. If not included herewith (or previously mailed), a Notice of Allowance (PTOL-85) or other appropriate communication will be mailed in due course. THIS NOTICE OF ALLOWABILITY IS NOT A GRANT OF PATENT RIGHTS. This application is subject to withdrawal from issue at the initiative of the Office or upon petition by the applicant. See 37 CFR 1.313 and MPEP 1308.		
1. This communication is responsive to <u>05/23/05</u> .		
2. The allowed claim(s) is/are <u>2, 6-16, and 18-26 renumbered 1-21</u> .		
3. 🗵 The drawings filed on 10 August 2000 are accepted by the Examiner.		
4. Acknowledgment is made of a claim for foreign priority under 35 U.S.C. § 119(a)-(d) or (f). a)		
 Attachment(s) 1. Notice of References Cited (PTO-892) 2. Notice of Draftperson's Patent Drawing Review (PTO-948) 3. Information Disclosure Statements (PTO-1449 or PTO/SB/0 Paper No./Mail Date 05/23/05 4. Examiner's Comment Regarding Requirement for Deposit of Biological Material 	6. ☐ Interview Summary Paper No./Mail Da 08), 7. ☐ Examiner's Amendr	te
		Pierre-Michel Bataille Primary Examiner Art Unit: 2186

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DETAILED ACTION

Allowable Subject Matter

- 1. Claims 2, 6-16, and 18-26 are allowed.
- 2. The following is an examiner's statement of reasons for allowance: none of the prior art of record teaches or suggests, independently or in combination, renders obvious the specific features as recited in independent claims 2, 7, 18, 19, and 20. The claims specifically recited "external storage device dynamically changing the sizes of the first and second areas of non-volatile semiconductor memory on the basis of an instruction of a host computer by a part which rewrites information indicating of the plural areas of the non-volatile semiconductor memory".

Known system recites dynamic partitioning of semiconductor memory system.

However, no prior art of record teaches or renders obvious the features of the claims, taken as a whole in light of the specification.

3. Any comments considered necessary by applicant must be submitted no later than the payment of the issue fee and, to avoid processing delays, should preferably accompany the issue fee. Such submissions should be clearly labeled "Comments on Statement of Reasons for Allowance."

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Conclusion

Any inquiry concerning this communication or earlier communications from the examiner should be directed to Pierre-Michel Bataille whose telephone number is (571) 272-4178. The examiner can normally be reached on Mon-Fri (8:30A to 5:00P).

If attempts to reach the examiner by telephone are unsuccessful, the examiner's supervisor, Matthew M. Kim can be reached on (571) 272-4182. The fax phone number for the organization where this application or proceeding is assigned is 703-872-9306.

Information regarding the status of an application may be obtained from the Patent Application Information Retrieval (PAIR) system. Status information for published applications may be obtained from either Private PAIR or Public PAIR. Status information for unpublished applications is available through Private PAIR only. For more information about the PAIR system, see http://pair-direct.uspto.gov. Should you have questions on access to the Private PAIR system, contact the Electronic Business Center (EBC) at 866-217-9197 (toll-free).

Pierre-Michel Bataille Primary Examiner Art Unit 2186

June 6, 2005

PIERRE BATAILLE PRIMARY EXAMINER